



Docket No.: 394-1597

3/Priority Papers  
7-16-94  
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Seiji SAWADA et al.

Serial No. 08/246,582

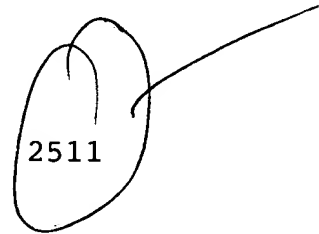
Filed: May 19, 1994

For: TEST CIRCUIT IN CLOCK SYNCHRONOUS SEMICONDUCTOR MEMORY  
DEVICE

:  
:  
:  
:  
:  
:  
:

Group Art Unit: 2511

Examiner:



TRANSMITTAL OF CERTIFIED PRIORITY DOCUMENT

Honorable Commissioner of  
Patents and Trademarks  
Washington, D.C. 20231

Sir:

7/5/94  
25C  
POPLK

0500  
2511  
002  
0-25-94

At the time of filing the above-identified application, a  
claim was made to the priority date(s) of the corresponding  
Japanese Patent Application Serial No. 5-122439  
filed May 25, 1993

We now submit the priority document(s) in support of this  
Claim of Priority. Acknowledgement is respectfully requested.

Respectfully submitted,

LOWE, PRICE, LEBLANC & BECKER

Stephen A. Becker  
Registration No. 26,527

99 Canal Center Plaza, Suite 300  
Alexandria, Virginia 22314  
(703) 684-1111 SAB:kh  
Enclosure  
Date: June 24, 1994

JUN 24 1994  
GROUP 2500